

AMENDMENT TRANSMITTAL LETTER (Large Entity) Applicant(s): Darren L. Anand, et al.	Docket No. BUR920030168US1 (17124)
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Application No. 10/707,071	Filing Date November 19, 2003	Examiner James C. Kerveros	Customer No. 23389	Group Art Unit 2138	Confirmation No. 1070
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Invention: **AUTOMATIC BIT FAIL MAPPING FOR EMBEDDED MEMORIES WITH CLOCK MULTIPLIERS**

COMMISSIONER FOR PATENTS:

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED

	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	25 -	25 =	0	x \$50.00	\$0.00
INDEP. CLAIMS	4 -	4 =	0	x \$210.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0.00

- ☒ No additional fee is required for amendment.
- ☐ Please charge Deposit Account No. _____ in the amount of _____
- ☐ A check in the amount of _____ to cover the filing fee is enclosed.
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 - ☒ Any patent application processing fees under 37 CFR 1.17.
- ☐ Payment by credit card. Form PTO-2038.

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 Signature

Dated: **October 15, 2007**

Steven Fischman
Registration No. 34,594

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